EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	2	"20050044521"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/29 07:43
S2	2	pcb same (low adj level adj detail)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/14 15:38
S3	11069	pcb same ((low near3 level near3 detail) or (trace near3 (width and (spac$3 or region or area))) or variable or rout$3 or plac$5)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/14 15:43
S4	1244	pcb same ((low near3 level near3 detail) or (trace near3 (width and (spac$3 or region or area))) or variable or rout$3 or plac$5) and constraint	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/14 15:43
S5	639	pcb same ((low near3 level near3 detail) or (trace near3 (width and (spac$3 or region or area))) or variable or rout$3 or plac$5) and constraint and module	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/14 15:43
S6	80	pcb same ((low near3 level near3 detail) or (trace near3 (width and (spac$3 or region or area))) or variable or rout$3 or plac$5) and constraint and module and "716"/$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/14 15:49
S7	17	pcb same ((low near3 level near3 detail) or (trace near3 (width and (spac$3 or region or area))) or variable or rout$3 or plac$5) same constraint same module	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/14 15:59
S8	9	pcb same module same constraint and ((low near3 level near3 detail) or (trace near3 (width and (spac$3 or region or area))) or variable or rout$3 or plac$5) and "716"/$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/14 16:08
S9	1	pcb same (design near3 (information or data or detail)) same module same constraint	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/14 16:09

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S10	9	pcb same design same (information or data or detail) same module same constraint	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/14 16:14
S11	1	pcb same (constraint near3 (creat$3 or generat$3 or convert$3)) same (module or megacell or block) and "716"/$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/14 16:21
S12	1	pcb same (constraint near3 (creat$3 or generat$3 or convert$3)) and (module or megacell or block) and "716"/$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/14 16:21
S13	28	pcb and (constraint near3 (creat$3 or generat$3 or convert$3)) and (module or megacell or block) and "716"/$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/14 16:28
S14	29	pcb and (constraint near3 (creat$3 or generat$3 or convert$3 or obtain$3)) and (module or megacell or block) and "716"/$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/15 07:41
S15	10	pcb and (constraint near3 (creat$3 or generat$3 or convert$3 or obtain$3)) and (module or megacell or block) and (design near3 (information or data or input or initial)) and "716"/$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/15 09:10
S16	0	(low adj level adj description) same design same (module or block or megacell) same constraint and pcb and "716"/$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/15 09:12
S17	0	(low adj level adj description) same design same (module or block or megacell) same constraint and "716"/$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/15 09:13
S18	0	(low adj level adj (description or information or data)) same design same (module or block or megacell) same constraint and "716"/$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/15 09:13
S19	0	(low adj level adj (description or information or data)) same (module or block or megacell) same constraint and "716"/$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/15 09:13

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S20	1	(low adj level adj description) same design same constraint and "716"/$. ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/15 09:23
S21	932	((low adj level adj description) or specification or information or data) same design same constraint and "716"/$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/15 09:24
S22	139	((low adj level adj description) or specification or information or data) same design same (constraint near3 (creat$3 or generat$3 or obtain$3)) and "716"/$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/15 09:25
S23	8	((low adj level adj description) or specification or information or data) same design same (constraint near3 (creat$3 or generat$3 or obtain$3)) and pcb and "716"/$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/28 15:59
S24	4	pcb same constraint same (information or data or description or specification) and (rout$3 or wir$3 or interconnect$3 or connect$3) and (placement or plac$3 or stack$3 or width or spac$3) and variable and (constraint same (schemat$5 or electric$5 or mechanic$5)) and "716"/$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/28 16:12
S25	10	pcb same constraint same (information or data or description or specification) and (rout$3 or wir$3 or interconnect$3 or connect$3) and (placement or plac$3 or stack$3 or width or spac$3) and variable and (constraint same (schemat$5 or electric$5 or mechanic$5))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/28 16:13
S26	1	"5901064".PN.	USPAT; USOCR	OR	ON	2006/03/29 07:36
S27	1	"6260177".PN.	USPAT; USOCR	OR	ON	2006/03/29 07:36

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S28	11116	(user or designer or operator) same (rule or requirement or manufactur$3 or fabricat$3 or (board adj size) or thickness or connector or electric$4 or crosstalk or noise or impedance or cost or layer or technolog$4) and (constraint or rout$3 or wir$3 or connect$3 or interconnect$3 or plac$3 or placement or stack) and variable and (pcb or (printed adj circuit adj board) or (wiring adj board))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/29 07:50
S29	303	(user or designer or operator) same (rule or requirement or manufactur$3 or fabricat$3 or (board adj size) or thickness or connector or electric$4 or crosstalk or noise or impedance or cost or layer or technolog$4) and (constraint or rout$3 or wir$3 or connect$3 or interconnect$3 or plac$3 or placement or stack) and variable and (pcb or (printed adj circuit adj board) or (wiring adj board)) and "716"/$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/29 07:50
S30	105	(user or designer or operator) same (rule or requirement or manufactur$3 or fabricat$3 or (board adj size) or thickness or connector or electric$4 or crosstalk or noise or impedance or cost or layer or technolog$4) and (constraint or rout$3 or wir$3 or connect$3 or interconnect$3 or plac$3 or placement or stack) and variable and (pcb or (printed adj circuit adj board) or (wiring adj board)) and compil$3 and "716"/$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/29 10:44
S31	0	((creat$3 or generat$3) near5 constraint near5 variable) same (PCB or (printed adj circuit adj board) or package or (PC adj board) or (printed adj board) or board) same ((description or specification or requirement or rule) near5 design) and (user or operator or designer) and "716"/$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/29 10:47
S32	0	((creat$3 or generat$3) near5 constraint near5 variable) same (PCB or (printed adj circuit adj board) or package or (PC adj board) or (printed adj board) or board) same ((description or specification or requirement or rule) near5 design) and (user or operator or designer)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/29 10:48

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S33	0	((creat$3 or generat$3) near5 constraint near5 variable) same (PCB or (printed adj circuit adj board) or package or (PC adj board) or (printed adj board) or board) and ((description or specification or requirement or rule) near5 design) and (user or operator or designer)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/29 10:48
S34	0	((creat$3 or generat$3) near5 constraint near5 variable) same (PCB or (printed adj circuit adj board) or package or (PC adj board) or (printed adj board) or board)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/29 10:48
S35	9	((creat$3 or generat$3) near5 constraint) same (PCB or (printed adj circuit adj board) or package or (PC adj board) or (printed adj board) or board) same variable	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/29 10:57
S36	89	((creat$3 or generat$3) near5 constraint) same (PCB or (printed adj circuit adj board) or package or (PC adj board) or (printed adj board) or board) and variable	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/29 12:02
S37	5	((creat$3 or generat$3) near5 constraint) same (PCB or (printed adj circuit adj board) or package or (PC adj board) or (printed adj board) or board) and variable and "716"/$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/29 10:57
S38	0	(PCB or (printed adj circuit adj board) or package or (PC adj board) or (printed adj board) or board) same (reus$5 or (us$3 near3 again)) same constrain same (description or information or specification) and variable	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/29 12:04
S39	6	(PCB or (printed adj circuit adj board) or package or (PC adj board) or (printed adj board) or board) same (reus$5 or (us$3 near3 again)) same constraint same (description or information or specification) and variable	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/29 12:09
S40	28	(PCB or (printed adj circuit adj board) or package or (PC adj board) or (printed adj board) or board) same (reus$5 or (us$3 near3 again)) and 716/4.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/29 12:11

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S41	17	(PCB or (printed adj circuit adj board) or package or (PC adj board) or (printed adj board) or board) same (reus$5 or (us$3 near3 again)) and constraint and variable and 716/4.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/29 12:12
S42	19	(PCB or (printed adj circuit adj board) or package or (PC adj board) or (printed adj board) or board) same (reus$5 or (us$3 near3 again)) and (constraint same creat$3 or generat$3) and variable and 716/4. ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/29 12:13
S43	12	(PCB or (printed adj circuit adj board) or package or (PC adj board) or (printed adj board) or board) same (reus$5 or (us$3 near3 again)) and (constraint same (creat$3 or generat$3)) and variable and 716/4. ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/29 13:33
S44	248	(PCB or (printed adj circuit adj board) or (PC adj board) or (printed adj board) or board) and (constraint near5 (creat$3 or generat$3 or mak$3 or produc$3)) and "716"/$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/29 15:44
S45	25	(PCB or (printed adj circuit adj board) or (PC adj board) or (printed adj board) or board) same (constraint near5 (creat$3 or generat$3 or mak$3 or produc$3)) and "716"/$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/29 13:54
S46	3	"5883807"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/29 13:54
S47	10336	(PCB or (printed adj circuit adj board) or (PC adj board) or (printed adj board) or board) and (constraint near5 (creat$3 or generat$3 or mak$3 or produc$3)) and convert$3and "716"/$. ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/29 15:44
S48	171	(PCB or (printed adj circuit adj board) or (PC adj board) or (printed adj board) or board) and (constraint near5 (creat$3 or generat$3 or mak$3 or produc$3)) and convert$3 and "716"/$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/29 15:44

| S49 | 11 | (PCB or (printed adj circuit adj board) or (PC adj board) or (printed adj board) or board) and (constraint near5 (creat$3 or generat$3 or mak$3 or produc$3)) same convert$3 and "716"/$.ccls. | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/29 15:45 |